

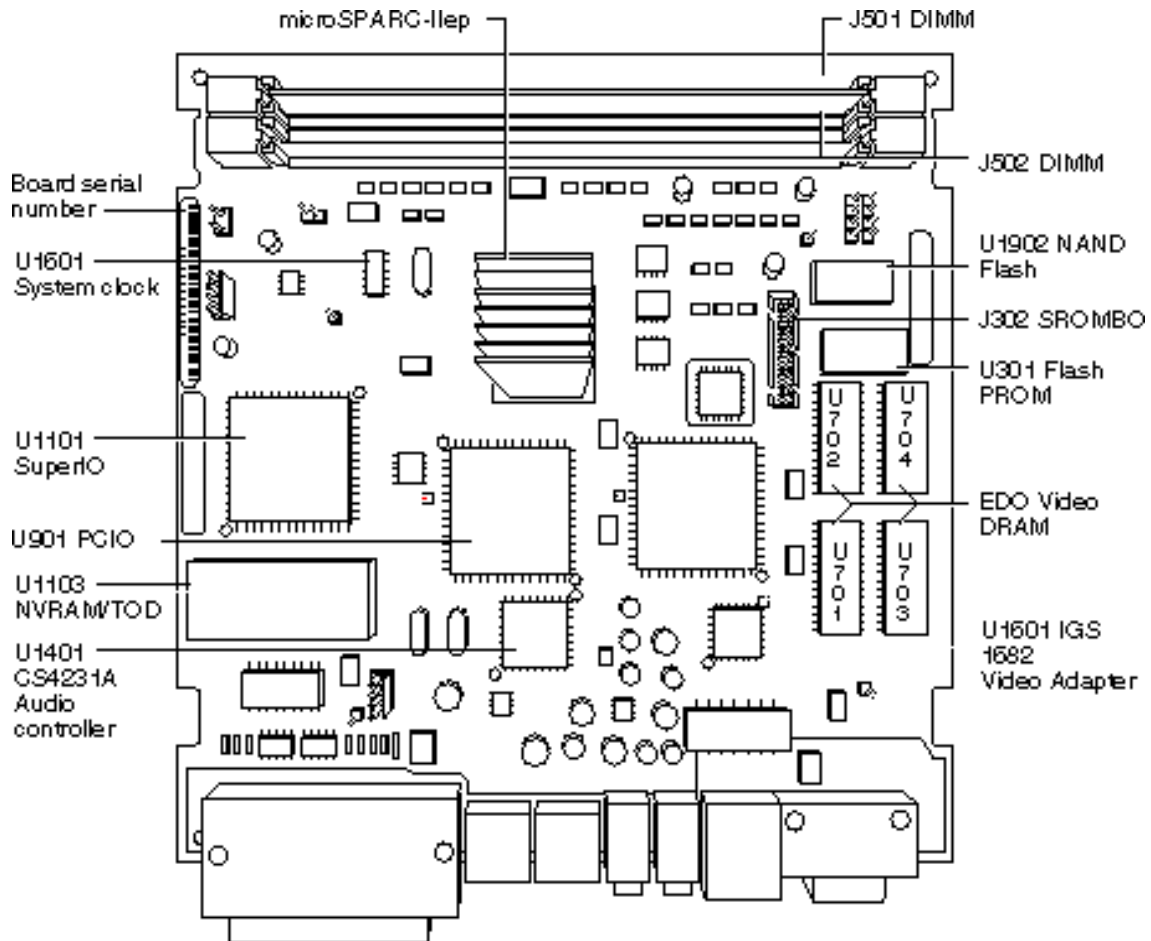
JavaEngine

JavaEngine 1

501-4628

16 MB FRU 32MB FRU

100MHz



Note –

1. Optional 8 MByte NAND flash (U1902) memory device enables on-board storage of JavaOS.
2. Board serial number indicates whether board contains NAND flash memory. Read the four digits after the 501 prefix of the number. If the number is 5014811, the board has NAND flash; if the number is 5014628, there is no NAND flash memory.
3. Host server requires Solaris 2.5.1 or later.
4. Two 168-pin DIMM sockets (J501, J502) provide a maximum of 64 MBytes of unbuffered main memory in either 16 MB or 32 MB DIMM configurations. DIMMs may be populated in any arrangement.
5. National Semiconductor SuperIO chip (U1101) connects to PCIO EBus2.

JavaEngine 1

501-4628

DRAM Configuration

There are many permutations of memory population that are acceptable to yield various amounts of memory less than the maximum 64 MBytes.

DRAM Size Options

DRAM Type	Total DRAM Size
2 Mbit x 64	16 MBytes
4 Mbit x 64	32 MBytes

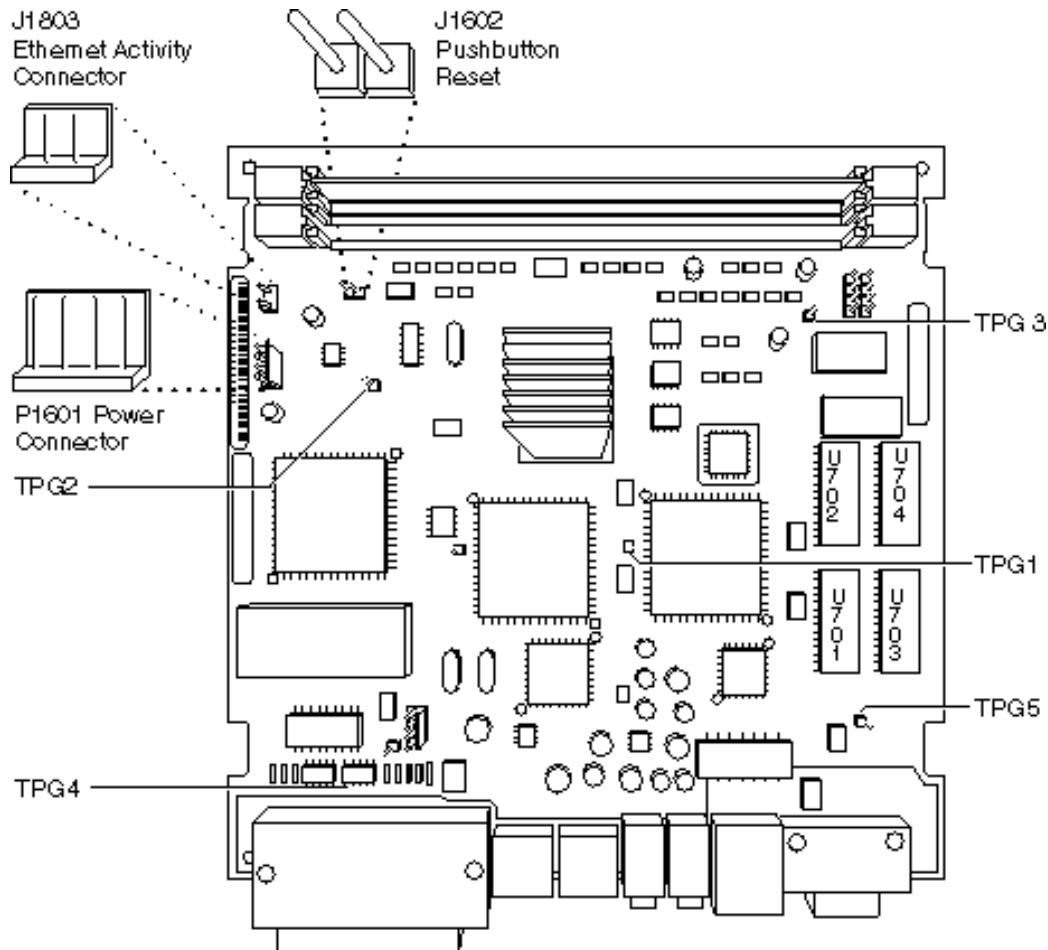
Note –

1. All DRAM must be of a speed rating consistent with the target design speed. The recommended access time (for 100 MHz) is 60 nanoseconds.
2. All DIMMs must be 64 bits wide, 3.3 V, 168 pins, unbuffered.
3. The maximum DIMM density is 32 MBytes (4 Mbit x 64). The use of any DIMM size over 32 MBytes may produce unpredictable results.
4. The minimal DIMM configuration (available from Samsung) is 16 MBytes configured as 2 Mbit x 64.
5. Refresh rates for all DRAM used must be known and the system must be tuned for the DRAM with the highest refresh rate.

JavaEngine 1

501-4628

Board Jumpers and Headers



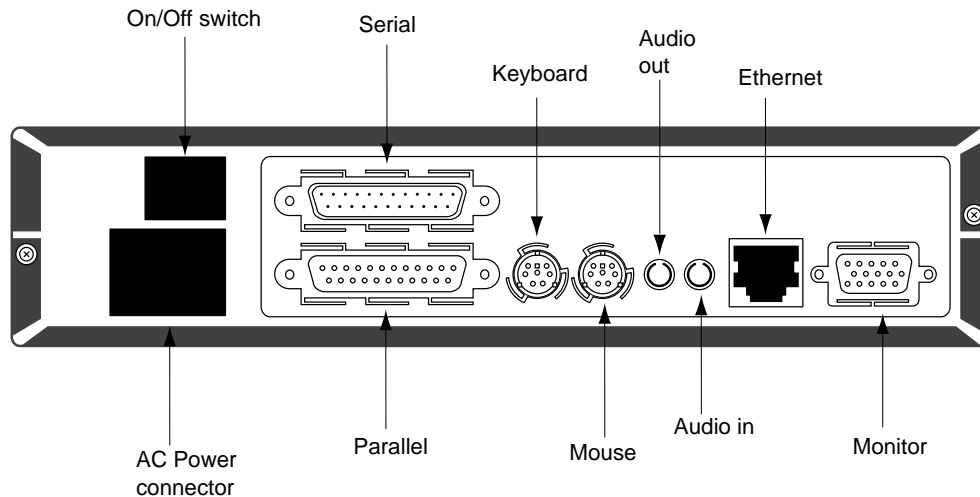
Note –

1. J1602 is a push-button reset connector for testing purposes.
2. TPG1-TPG5 are single pin test ground points for the JavaEngine 1 board.
3. J301 is a three-pin header for external LED. LED is steady-state ON when board is powered on and flashes to indicate Ethernet activity.
4. J1601 (JTAG) is an eight-pin connector for the joint test action group (JTAG) boundary scan tests on microSPARC-IIep microprocessor.
5. J1803 Ethernet two-pin header that attaches to an LED status light to indicate Ethernet activity.
6. P1601 power connector distributes power to the board from the power supply.

JavaEngine 1

501-4628

JavaEngine 1 I/O Panel

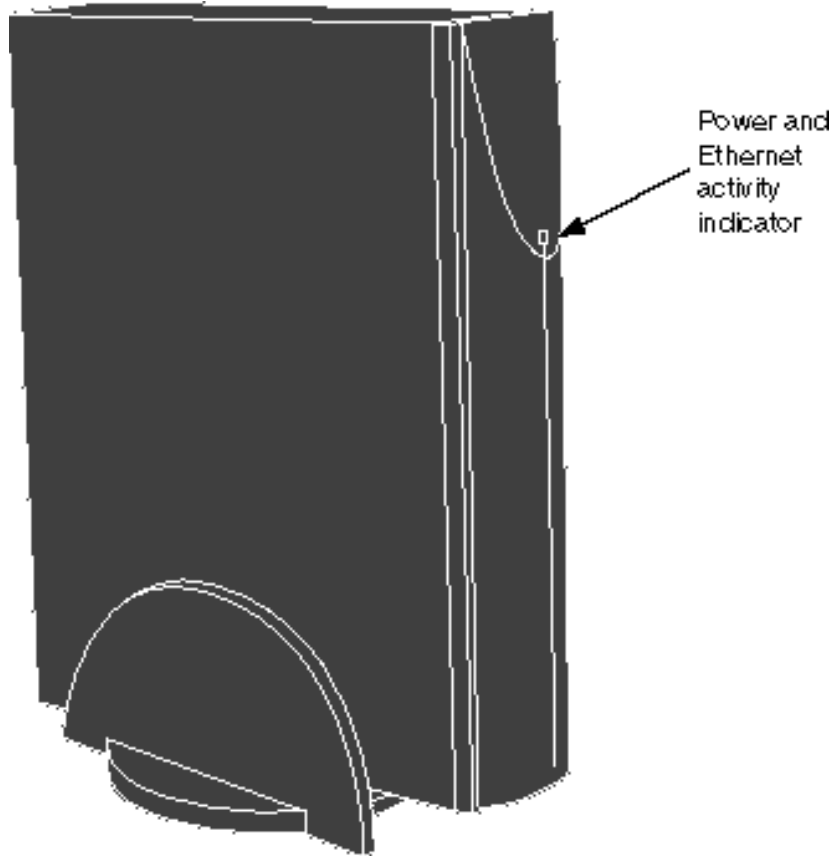


Note –

1. Serial is 25-pin plug (male) connector. It provides standard RS-232 connection.
2. Parallel is standard PC-style 25-pin receptacle (female) connector with IEEE 1284 interface.
3. Keyboard is PS/2-style (6-pin circular mini-DIN) connector.
4. Mouse is PS/2-style (6-pin circular mini-DIN) connector.
5. Audio out is stereo connector playback with 16-bit audio converters.
6. Audio in connector is not currently supported. Reserved for future use.
7. Ethernet is RJ45 connector with 10/100 Mbit autosensing support.
8. Video is standard PS-style SVGA connector that provides multiple resolution options.

JavaEngine 1

501-4628
JavaEngine 1 Chassis Enclosure



Note –

The power and Ethernet LED indicates both power and Ethernet activity.